MAHARSHI DAYANAND UNIVERSITY, ROHTAK (HARYANA) SCHEME OF STUDIES & EXAMINATION FOR B. TECH. (ELECTRONICS AND COMPUTER ENGG.) SEMESTER-VII

'F' Scheme effective from 2011-12

Sr.	Course No.	Course Title	Teaching Schedule		ule	Marks Examination		Total	Duration		
No.			L	T	P	Tot al	of class work	Theory	Practical	Marks	Of Exam
1	EC-711-F	System Simulation and Modeling	3	1	-	4	50	100	-	150	3
2	EC-712-F	Compiler Design	3	1	-	4	50	100	-	150	3
3	IT-401-F	Data Warehousing & Data Mining	3	1	-	4	50	100	-	150	3
4		Elective I*	3	1	-	4	50	100	-	150	3
5	EE-402-F	Wireless Communication	3	1	-	4	50	100	-	150	3
6	EC-715-F	System Simulation and Modeling Lab	-	-	3	3	50	-	50	100	3
7	CSE-411-F	Compiler Design Lab	-	-	3	3	50	-	50	100	3
8	CSE-315-F	Practical Training-II	-	-	2	2	-	-	-	-	3
9	CSE-413-F	Project	-	-	4	4	50	-	50	100	3
		TOTAL	15	5	12	32	400	500	150	1050	

NOTE:

- 1. Students will be allowed to use non-programmable scientific calculator. However, sharing of calculator will not be permitted in the examination.
- 2. *Student will be permitted to opt for any one elective run by the other departments. However, the departments will offer only those electives for which they have expertise. The choice of the students for any elective shall not be a binding for the department to offer, if the department does not have expertise.
- 3. Assessment of Practical Training-I, undergone at the end of IV semester, will be based on seminar, viva-voce, report and certificate of practical training obtained by the student from the industry. According to performance letter grades A, B, C, F are to be awarded. A student who is awarded 'F' grade is required to repeat Practical Training.
- 4. Project load will be treated as 2 hours per week for Project Coordinator and 1 hour for each participating teacher. Project will commence in VII semester where the students will identify the Project problem, complete the design/procure the

material/start the fabrication/complete the survey etc., depending upon the nature of the problem. Project will continue in VIII semester.

Elective-I

1.	PHY-451-F	NANO Technology
2.	HUM-457-F	Business Communication
3.	CSE-432-F	Digital Image Processing
4.	CSIT-401-F	Mobile Computing
5.	CSIT-402-F	Cyber Crime Investigation & Cyber Forensics
6.	IT-472-F	Introduction to VLSI Design
7.	CSE-446-F	Natural Language Processing
8.	EC-821-F	Digital IC Design
9.	CSE-407-F	Neural Networks

MAHARSHI DAYANAND UNIVERSITY, ROHTAK (HARYANA) SCHEME OF STUDIES & EXAMINATION FOR B. TECH. (ELECTRONICS AND COMPUTER ENGG.) SEMESTER -VIII

'F' Scheme Effective from 2011-2012

Sr.	Course No.	Course Title	Teaching Schedule			Marks Examination			Total Duration		
No.			L	Т	P	Total	of class work	Theory	Practical	Marks	Of Exam
1	CSE-305-F	Analysis and Design of Algorithms	3	1	-	4	50	100	-	150	3
2	EC-812-F	Satellite Communication	3	1	-	4	50	100	-	150	3
3		Elective-1	3	1	-	4	50	100	-	150	3
4		Elective-2	3	1	-	4	50	100	-	150	3
5	EC-815-F	Satellite Communication Lab	-	-	2	4	50	-	50	100	3
6	CSIT-410- F	Seminar	-	-	4	4	50	-	-	50	3
7	CSE-413-F	Project	-	-	8	8	100	-	100	200	3
8	GP-SSE- 402-F	General Proficiency	-	-	-	-	-	-	-	-	
		TOTAL	12	4	14	32	400	400	150	950	

NOTE:-

- 1. Students will be allowed to use the non-programmable scientific calculator. However, sharing of calculator will not be permitted in the examination.
- 2. A team consisting of Principal/Director, HOD of concerned department and external examiner appointed by University shall carry out the evaluation of the student for his/her General Fitness for the Profession.
- 3. Project load will be treated as 2 hrs. per week for the project coordinator and 1 hour for each participating teacher. Project involving design, fabrication, testing, computer simulation, case studies etc., which has been commenced by students in VII semester, will be completed in VIII semester.

ELECTIVE-1

Language Skill for Engineers 1. HUM-451-F Human Computer Interaction 2. CSE-442-F 3. EC-818-F Telecommunication-Switching and Networks 4. EC-819-F Mixed Signal IC Design 5. EC-820-F Low Power Design 6. EC-722-F Bioinformatics 7. EC-822-F Analog IC Design 8. EC-823-F MEMS

Elective-II

1.	CSE-444-F	Fuzzy Logic
2.	CSIT-404	Parallel Computation & Applications
3.	CSE-403-F	Software Project Management
4.	IT-471-F	Management Information System
5.	IT-464-F	Network Security Management
6.	CSE-401-F	Advanced Computer Architecture
7.	CSE-402-F	Distributed Operating Systems
8.	CH-453-F	Pollution & Control