

M.D. UNIVERSITY, ROHTAK
SCHEME OF STUDIES AND EXAMINATION
B.TECH (Electronics and Computer Engineering)
SEMESTER 3rd and 4th
Scheme effective from 2019-20



COURSE CODE AND DEFINITIONS

Course Code	Definition
L	Lecture
T	Tutorial
P	Practical
BSC	Basic Science Courses
ESC	Engineering Science Courses
HSMC	Humanities and Social Sciences including Management courses
PCC	Professional Core Courses
LC	Laboratory Courses
MC	Mandatory Courses
PT	Practical Training
S	Seminar

B.Tech. (Electronics and Computer Engineering)**Scheme of Studies/Examination****Semester- 3 w.e.f. 2019-20**

Sr. No.	Course Code	Course Title	Hours per week			Total Contact Hrs. per week	Credit	Examination Schedule (Marks)				Duration of Exam (Hours)
			L	T	P			Mark of Class work	Theory	Practical	Total	
1	PCC-CSE-201G	Database Management Systems (Common with CSE)	3	0	0	3	3	25	75		100	3
2	PCC-CSE-203G	Data Structures & Algorithms (Common with CSE)	3	0	0	3	3	25	75		100	3
3	PCC-CSE-205G	Digital Electronics (Common with CSE)	3	0	0	3	3	25	75		100	3
4	PCC-ECE201G	Electronic Devices (Common with ECE)	2	0	0	2	2	25	75		100	3
5	BSC-MATH-203G	Mathematics - III (Multivariable Calculus and Differential Equations) (Common with CSE)	2	0	0	2	2	25	75		100	3
6	HSMC-01G	Economics for Engineers(Common with CSE)	3	0	0	3	3	25	75		100	3
7	LC-CSE-209G	Database Management Systems LAB(Common with CSE)	0	0	4	4	2	25		25	50	3
8	LC-CSE-211G	Digital Electronics LAB(Common with CSE)	0	0	4	4	2	25		25	50	3
9	LC-CSE-213G	Data Structures & Algorithms LAB Using C(Common with CSE)	0	0	4	4	2	25		25	50	3
10	LC-ECE203G	Electronic Devices Labs(Common with ECE)	0	0	2	2	1	25		25	50	3
Total							23				800	

B.Tech. (Electronics and Computer Engineering)

Scheme of Studies/Examination

Semester- 4 w.e.f. 2019-20

Sr. No.	Course Code	Course Title	Hours per week			Total Contact Hrs. per week	Credit	Examination Schedule (Marks)				Duration of Exam (Hours)
			L	T	P			Mark of Class work	Theory	Practical	Total	
1	PCC-CSE-202G	Discrete Mathematics (Common with CSE)	3	1	0	3	4	25	75		100	3
2	PCC-CSE-204G	Computer Organization & Architecture(Common with CSE)	3	0	0	3	3	25	75		100	3
3	PCC-CSE-206G	Operating System(Common with CSE)	3	0	0	3	3	25	75		100	3
4	PCC-CSE-208G	Object Oriented Programming(Common with CSE)	3	0	0	3	3	25	75		100	3
5	HSMC-02G	Organizational Behaviour	3	0	0	3	3	25	75		100	3
6	* MC-106G	Environmental Sciences	3	0	1	4	0	25	75			3
7	PCC-ECE-202G	Communication System(Common with ECE)	2	0	0	2	2	25	75		100	3
8	LC-CSE-212G	Operating System LAB(Common with CSE)	0	0	4	4	2	25		25	50	3
9	LC-CSE-214G	Object Oriented Programming LAB Using C++(Common with CSE)	0	0	4	4	2	25		25	50	3
10.	LC-ECE-204G	Communication System Lab(Common with ECE)	0	0	2	2	1	25		25	50	3
Total							23				750	

***MC-106G** is a mandatory non –credit course in which the students will be required passing marks in theory.

NOTE: At the end of 4th semester each student has to undergo Practical Training of 4/6 weeks in an Industry/ Institute/ Professional Organization/ Research Laboratory/ training centre etc. and submit typed report along with a certificate from the organization & its evaluation shall be carried out in the 5th Semester.

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Database Management System

Course code	PCC-CSE-201G			
Category	Professional Core Course			
Course title	Database Management System			
Scheme and Credits	L	T	P	Credits
	3	0		3
Class work	25 Marks			
Exam	75 Marks			
Total	100 Marks			
Duration of Exam	03 Hours			

Objectives of the course

- To understand the different issues involved in the design and implementation of a database system.
- To study the physical and logical database designs, database modeling, relational, hierarchical, and network models
- To understand and use data manipulation language to query, update, and manage a database
- To develop an understanding of essential DBMS concepts such as: database security, integrity, concurrency, distributed database, and intelligent database, Client/Server (Database Server), Data Warehousing.
- To design and build a simple database system and demonstrate competence with the fundamental tasks involved with modeling, designing, and implementing a DBMS.

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Unit: 1

Database system architecture: Data Abstraction, Data Independence, Data Definition Language (DDL), Data Manipulation Language (DML). **Data models:** Entity-relationship model, network model, relational and object oriented data models, integrity constraints, data manipulation operations.

Unit: 2

Relational query languages: Relational algebra, Tuple and domain relational calculus, SQL3, DDL and DML constructs, Open source and Commercial DBMS - MYSQL, ORACLE, DB2, SQL server.

Relational database design: Domain and data dependency, Armstrong's axioms, Normal forms, Dependency preservation, Lossless design.

Query processing and optimization: Evaluation of relational algebra expressions, Query equivalence, Join strategies, Query optimization algorithms.

Unit: 3

Storage strategies: Indices, B-trees, hashing,

Transaction processing: Concurrency control, ACID property, Serializability of scheduling, Locking and timestamp based schedulers, Multi-version and optimistic Concurrency Control schemes, Database recovery.

Unit: 4

Database Security: Authentication, Authorization and access control, DAC, MAC and RBAC models, Intrusion detection, SQL injection.

Advanced topics: Object oriented and object relational databases, Logical databases, Web databases, Distributed databases, Data warehousing and data mining.

Suggested books:

“Database System Concepts”, 6th Edition by Abraham Silberschatz, Henry F. Korth, S. Sudarshan, McGraw-Hill.

Suggested reference books

“Principles of Database and Knowledge – Base Systems”, Vol 1 by J. D. Ullman, Computer Science Press.

“Fundamentals of Database Systems”, 5th Edition by R. Elmasri and S. Navathe, Pearson Education

“Foundations of Databases”, Reprint by Serge Abiteboul, Richard Hull, Victor Vianu, Addison-Wesley

Course Outcomes

1. For a given query write relational algebra expressions for that query and optimize the developed expressions
2. For a given specification of the requirement, design the databases using E R method and normalization.
3. For a given specification, construct the SQL queries for Open source and Commercial DBMS -MYSQL, ORACLE, and DB2.
4. For a given query optimize its execution using Query optimization algorithms
5. For a given transaction-processing system, determine the transaction atomicity, consistency, isolation, and durability.
6. Implement the isolation property, including locking, time stamping based on concurrency control and Serializability of scheduling.

Data Structure & Algorithms

Course code	PCC-CSE-203G				
Category	Professional Core Course				
Course title	Data Structure & Algorithms				
Scheme and Credits	L	T	P	Credits	
	3	0		3	
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Objectives of the course:

1. To impart the basic concepts of data structures and algorithms.
 - To understand concepts about searching and sorting techniques
 - To understand basic concepts about stacks, queues, lists, trees and graphs.
 - To enable them to write algorithms for solving problems with the help of fundamental data structures

Unit 1:

Introduction: Basic Terminologies: Concept of Data Structure, Choice of right Data Structure, Algorithms, how to design and develop algorithm, Complexity of algorithm. Operations: insertion, deletion, traversal etc.; Analysis of an Algorithm, **Searching:** Linear Search and Binary Search Techniques and their complexity analysis.

Unit 2:

Stacks and Queues: Stack and its operations: Algorithms and their complexity analysis, Applications of Stacks: Expression Conversion and evaluation -corresponding algorithms and complexity analysis. queue, Types of Queue: Simple Queue, Circular Queue, Priority Queue; Operations on each types of Queues: Algorithms and their analysis.

Unit 3:

Linked Lists: Singly linked lists: Representation in memory, Algorithms of several operations: Traversing, Searching, Insertion into, Deletion from linked list; Linked representation of Stack and Queue, Header nodes, Doubly linked list: operations on it and algorithmic analysis; Circular Linked Lists: all operations their algorithms and the complexity analysis.

Trees: Basic Tree Terminologies, Different types of Trees: Binary Tree, Threaded Binary Tree, Binary Search Tree, AVL Tree; Tree operations on each of the trees and their algorithms with complexity analysis. Applications of Binary Trees. B Tree, B+ Tree: definitions, algorithms and analysis.

Unit 4:

Sorting and Hashing: Objective and properties of different sorting algorithms: Selection Sort, Bubble Sort, Insertion Sort, Selection Sort Quick Sort, Merge Sort, Heap Sort; Performance and Comparison among all the methods.

Graph: Basic Terminologies and Representations, Graph search and traversal algorithms and complexity analysis.

Suggested books:

“Fundamentals of Data Structures”, Illustrated Edition by Ellis Horowitz, Sartaj Sahni, Computer Science Press.

Suggested reference books:

Algorithms, Data Structures, and Problem Solving with C++”, Illustrated Edition by Mark Allen Weiss, Addison-Wesley Publishing Company

“How to Solve it by Computer”, 2nd Impression by R.G. Dromey, Pearson Education.

Course outcomes

1. For a given algorithm student will able to analyze the algorithms to determine the time and computation complexity and justify the correctness.
2. For a given Search problem (Linear Search and Binary Search) student will able to implement it.
3. For a given problem of Stacks, Queues and linked list student will able to implement it and analyze the same to determine the time and computation complexity.
4. Student will able to write an algorithm Selection Sort, Bubble Sort, Insertion Sort, Quick Sort, Merge Sort, Heap Sort and compare their performance in term of Space and Time complexity.
5. Student will able to implement Graph search and traversal algorithms and determine the time and computation complexity.

DIGITAL ELECTRONICS

Course code	PCC-CSE-205G				
Category	Professional Core Course				
Course title	Digital Electronics				
Scheme and Credits	L	T	P	Credits	Semester 3
	3	0	0	3	
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

UNIT 1:

FUNDAMENTALS OF DIGITAL SYSTEMS AND LOGIC FAMILIES

Digital signals, digital circuits, AND, OR, NOT, NAND, NOR and Exclusive-OR operations, Boolean algebra, examples of IC gates, number systems-binary, signed binary, octal hexadecimal number, binary arithmetic, one's and two's complements arithmetic, codes, error detecting and correcting codes.

UNIT 2:

COMBINATIONAL DIGITAL CIRCUITS

Standard representation for logic functions, K-map representation, and simplification of logic functions using K-map, minimization of logical functions. Don't care conditions, Multiplexer, De-Multiplexer/Decoders, Adders, Subtractors, BCD arithmetic, carry look ahead adder, serial adder, ALU, elementary ALU design, popular MSI chips, digital comparator, parity checker/generator, code converters, priority encoders, decoders/drivers for display devices, Q-M method of function realization.

UNIT 3:

SEQUENTIAL CIRCUITS AND SYSTEMS

A 1-bit memory, the circuit properties of Bistable latch, the clocked SR flip flop, J- K-T and D types flip flops, applications of flip flops, shift registers, applications of shift registers, serial to parallel converter, parallel to serial converter, ring counter, sequence generator, ripple (Asynchronous)

counters, synchronous counters, counters design using flip flops, special counter IC's, asynchronous sequential counters, applications of counters.

UNIT 4:

A/D AND D/A CONVERTERS

Digital to analog converters: weighted resistor/converter, R-2R Ladder D/A converter, specifications for D/A converters, examples of D/A converter ICs, sample and hold circuit, Analog to digital converters: quantization and encoding, parallel comparator A/D converter, successive approximation A/D converter, counting A/D converter, dual slope A/D converter,

SEMICONDUCTOR MEMORIES AND PROGRAMMABLE LOGIC DEVICES

Memory organization and operation, expanding memory size, classification and characteristics of memories, sequential memory, read only memory (ROM), read and write memory(RAM), content addressable memory (CAM), ROM as a PLD, Programmable logic array, Programmable array logic, complex Programmable logic devices (CPLDS), Field Programmable Gate Array (FPGA).

Course Outcomes:

At the end of this course, students will demonstrate the ability to

1. Understand working of logic families and logic gates.
2. Design and implement Combinational and Sequential logic circuits.
3. Understand the process of Analog to Digital conversion and Digital to Analog conversion.
4. Use PLDs to implement the given logical problem.

REFERENCES:

1. R. P. Jain, "Modern Digital Electronics", McGraw Hill Education, 2009.
2. M. M. Mano, "Digital logic and Computer design", Pearson Education India, 2016.
3. A. Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, 2016.
4. Nasib Singh Gill and J B Dixit, "Digital Design and Computer Organization", University Science Press, New Delhi

PCC-ECE201G

Electronic Devices

L T P Credits
3 0 - 03

Sessional Marks: 25

Theory Marks: 75

Duration of Exams: 3 Hours

Course Objective: The objectives of this course are as under:

1. To provide explanation about the operation of all the important electronic devices
2. To study and understand the I/O behavior of various electronics devices to variable inputs
3. To demonstrate how electronic devices are used to design efficient electronic applications

Unit 1

Basic Semiconductor And Pn-Junction Theory: Introduction, Atomic Structure, Band Theory of Semiconductors, Covalent Bond, Metals, Insulators & Semiconductors, Effect of Temperature on Conduction, Drift Current, Donor & Acceptor Impurities in Semiconductor, Law Of Mass Action, Hall's Effect, Hall Coefficient & Mobility, Poisson and continuity equation.

Characteristics Of Diode: PN-Junction, Construction Types, Unbiased Junction, Biased Junction, Space Charge Region, Diode Characteristics & Parameters, Diode Capacitance, Diode Resistance, DC And AC Load Lines, Diode Testing, Zener And Avalanche Breakdown Diodes, Tunnel Diode, Temperature Characteristics of Diode, Reverse Recovery Time, Switching Characteristics of Diode.

Unit 2

Diode Applications: Half Wave, Full Wave Center Tapped, Full Wave Bridge (Rectification), Series Clipping Circuit, Shunt Clipping Circuit, Clamping Circuit, Bridge Voltage Doubler, Filtering Circuit Using Capacitor & Inductor.

Junction Transistor: Introduction, Construction Of Junction Transistor, Circuit Symbols, Transistor Operation, Unbiased Transistor, Operation Of Biased Transistor, Transistor Current Components, DC & AC Load Line, Operating Point, Transistor Configuration CB, CE, CC, Input/Output Characteristics, Early Effect (Base Width Modulation), Eber's-Moll-Model of Transistor, Maximum Rating of Transistor, Transistor Testing, Transistor as an Amplifier, Transistor as Oscillator.

Unit 3

Bjt Biasing: Bias Stability, Instability Due To β , Thermal Stability, Stability Factor, Fixed Biased Circuits, Effect of Emitter Resistor, Collector to Base Bias, Voltage Divide Biasing, Advantage & drawbacks of Biasing Techniques, Stability Factor calculation of Biasing Techniques, Bias Compensation by various device, Thermal Runway, Transistor Dissipation, Thermal Resistance, Condition of Thermal Stability

Small Signal Circuit: Two Port Network, Hybrid (H-Parameter) Model, Typical Values of H-Parameter Model, Conversion of CE, CB, CC Configuration to Equivalent Hybrid Model, CB Circuit Analysis, CE circuit with & without R_E analysis, CC circuit analysis, Analysis of CE, CB & CC Configuration with approximate Hybrid Model, Miller's Theorem, Dual of Miller Theorem.

Unit 4

FET: Introduction, The Junction FET, Basic Construction, Operation, P- Channel FET, N-Channel FET, High Frequency Model of FET, Low Frequency FET Amplifiers, Transfer Characteristics of FET, MOSFET, Enhancement Mode, Depletion Mode of FET, Circuit Symbol of MOSFET, V-MOSFET.

Special Semiconductor Devices: Optoelectronic Devices, Photoconductors, Photo Diode, Photo Transistor, Photo Voltaic Sensor, Photo Emission, Solar Cells, LED, LCD, Laser Diode, Schottky Diode, SCR, TRIAC, DIAC, UJT, Single Electron Transistor. Infrared LEDs, IGBT, Opto Coupler.

Text/Reference Books:

1. Basic Electronics By Debashion DE. – Pearson Education.
2. Electronics Device & Circuit, By Robert Boylestad, Louis Nashelsky, 11th Edition, Pearson Education, 2015.
3. Electronics Device Circuit By David.A.Bell -- Oxford
4. Integrated Electronics By Millman Halkias -- TMH.
5. Electronics Device & Circuit By Dharam Raj Cheruku -- Pearson Education.
6. Electronics Device & Circuit By B.P Singh and Rekha Singh 2nd Edition – Pearson Education.

Course Outcomes: At the end of the course, students will be able to:

1. Understand the operation of all the important electronic devices
2. Understand the I/O behavior of various electronics devices to variable inputs
3. Understand the design of efficient electronic applications

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Mathematics-III (Multivariable Calculus and Differential Equations)

Course code	BSC-MATH-203G				
Category	Basic Science Course				
Course title	Mathematics-III (Multivariable Calculus and Differential Equations)				
Scheme and Credits	L	T	P	Credits	Semester 3
	2	0		2	
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Unit-I

Multivariable Differential Calculus: Limit, Continuity and Partial derivatives, Homogeneous functions, Euler's Theorem, Total derivative, Maxima, Minima and Saddle points, Lagrange's method of undetermined multipliers

Unit-II

Multivariable Integral Calculus: Double integral, Change of order of integration, Change of variables, Applications of double integral to find area enclosed by plane curves, Triple integral

Unit-III

Ordinary Differential Equations of first order: Linear and Bernoulli's equations, Exact differential equations, Equations reducible to exact differential equations, Applications of differential equations of first order and first degree to simple electric circuits, Newton's law of cooling, Heat flow and Orthogonal trajectories

Unit-IV

Ordinary Differential equations of second and higher order: Linear differential equations of second and higher order, Complete solution, Complementary function and Particular integral, Method of variation of parameters to find particular integral, Cauchy's and Legendre's linear equations, Simultaneous linear differential equations with constant coefficients, Applications of linear differential equations to oscillatory electric circuits

Reference Books:

1. G.B. Thomas and R.L. Finney, Calculus and Analytic geometry, Pearson Education.
2. Erwin Kreyszig, Advanced Engineering Mathematics, John Wiley & Sons.
3. B. V. Ramana, Higher Engineering Mathematics, Tata McGraw-Hill Publishing Company Limited.
4. N. P. Bali and Manish Goyal, A text book of Engineering Mathematics, Laxmi Publications.
5. B. S. Grewal, Higher Engineering Mathematics, Khanna Publishers.
6. W. E. Boyce and R. C. DiPrima, Elementary Differential Equations and Boundary Value Problems, Wiley India.
7. S. L. Ross, Differential Equations, Wiley India.
8. E. A. Coddington, An Introduction to Ordinary Differential Equations, Prentice Hall India.
9. E. L. Ince, Ordinary Differential Equations, Dover Publications

Course Outcomes

The students will learn:

1. To deal with functions of several variables and evaluate partial derivative.
2. The mathematical tools needed in evaluating multiple integrals and their usage.
3. The effective mathematical tools for the solutions of ordinary differential equations that model physical processes.

ECONOMICS FOR ENGINEERS

Course code	HSMC- 01G				
Category	Humanities/ Social Sciences/ Management				
Course title	Economics For Engineers				
Scheme and Credits	L	T	P	Credits	
	3	0	0	3	
Branches (B. Tech.)	Common For All Branches				
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Course Objectives:

1. Acquaint the students to basic concepts of economics and their operational significance.
2. To stimulate the students to think systematically and objectively about contemporary economic problems.

UNIT-1

Definition of Economics- Various definitions, types of economics- Micro and Macro Economics, nature of economic problem, Production Possibility Curve, Economic laws and their nature, Relationship between Science, Engineering, Technology and Economic Development.

Demand- Meaning of Demand, Law of Demand, Elasticity of Demand- meaning, factors effecting it, its practical application and importance,

UNIT 2

Production- Meaning of Production and factors of production, Law of variable proportions, and Returns to scale, Internal external economies and diseconomies of scale. Various concepts of cost of production- Fixed cost, Variable cost, Money cost, Real cost, Accounting cost, Marginal cost, Opportunity cost. Shape of Average cost, Marginal cost, Total cost etc. in short run and long run.

UNIT-3

Market- Meaning of Market, Types of Market- Perfect Competition, Monopoly, Monopolistic Competition and Oligopoly (main features).

Supply- Supply and law of supply, Role of demand & supply in price determination and effect of changes in demand and supply on prices.

UNIT-4

Indian Economy- Nature and characteristics of Indian economy as under developed, developing and mixed economy (brief and elementary introduction), Privatization - meaning, merits and demerits.

Globalization of Indian economy - merits and demerits.

Banking- Concept of a Bank, Commercial Bank- functions, Central Bank- functions, Difference between Commercial & Central Bank.

COURSE OUTCOMES:

1. The students will able to understand the basic concept of economics.
2. The student will able to understand the concept of production and cost.
3. The student will able to understand the concept of market.
4. The student will able to understand the concept of privatization, globalization and banks.

REFERENCES:

1. Jain T.R., Economics for Engineers, VK Publication.
2. Chopra P. N., Principle of Economics, Kalyani Publishers.
3. Dewett K. K., Modern economic theory, S. Chand.
4. H. L. Ahuja., Modern economic theory, S. Chand.
5. Dutt Rudar & Sundhram K. P. M., Indian Economy.
6. Mishra S. K., Modern Micro Economics, Pragati Publications.
7. Singh Jaswinder, Managerial Economics, dreamtech press.
8. A Text Book of Economic Theory Stonier and Hague (Longman's Landon).
9. Micro Economic Theory – M.L. Jhingan (S.Chand).
10. Micro Economic Theory - H.L. Ahuja (S.Chand).
11. Modern Micro Economics : S.K. Mishra (Pragati Publications).
12. Economic Theory - A.B.N. Kulkarni & A.B. Kalkundrikar (R.Chand & Co).

Database Management System Lab

Course code	LC-CSE-209G				
Category	Professional Core Course				
Course title	Database Management System Lab				
Scheme and Credits	L	T	P	Credits	
	0	0	4	2	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	25 Marks				
Total	50 Marks				
Duration of Exam	03 Hours				

Course Objectives:

- Keep abreast of current developments to continue their own professional development
- To engage themselves in lifelong learning of Database management systems theories and technologies this enables them to pursue higher studies.
- To interact professionally with colleagues or clients located abroad and the ability to overcome challenges that arises from geographic distance, cultural differences, and multiple languages in the context of computing.
- Develop team spirit, effective work habits, and professional attitude in written and oral forms, towards the development of database applications.

Contents:

- i. Creation of a database and writing SQL queries to retrieve information from the database.
- ii. Performing Insertion, Deletion, Modifying, Altering, Updating and Viewing records based on conditions.
- iii. Creation of Views, Synonyms, Sequence, Indexes, Save point.
- iv. Creating an Employee database to set various constraints.
- v. Creating relationship between the databases.
- vi. Study of PL/SQL block.
- vii. Write a PL/SQL block to satisfy some conditions by accepting input from the user.
- viii. Write a PL/SQL block that handles all types of exceptions.
- ix. Creation of Procedures.
- x. Creation of database triggers and functions

- xi. Mini project (Application Development using Oracle/ MySQL)
 - a) Inventory Control System
 - b) Material Requirement Processing.
 - c) Hospital Management System.
 - d) Railway Reservation System.
 - e) Personal Information System.
 - f) Web Based User Identification System.
 - g) Time Table Management System.
 - h) Hotel Management

Digital Electronics Lab

Course code	LC-CSE-211G				
Category	Professional Core Course				
Course title	Digital Electronics Lab				
Scheme and Credits	L	T	P	Credits	Semester-3
	0	0	4	2	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	25 Marks				
Total	50 Marks				
Duration of Exam	03 Hours				

Implementation all experiments with help of Bread- Board.

1. Study of Logic Gates: Truth-table verification of OR, AND, NOT, XOR, NAND and NOR gates; Realization of OR, AND, NOT and XOR functions using universal gates.
2. Half Adder / Full Adder: Realization using basic and XOR gates.
3. Half Subtractor / Full Subtractor: Realization using NAND gates.
4. 4-Bit Binary-to-Gray & Gray-to-Binary Code Converter: Realization using XOR gates.
5. 4-Bit and 8-Bit Comparator: Implementation using IC7485 magnitude comparator chips.
6. Multiplexer: Truth-table verification and realization of Half adder and Full adder.
7. Demultiplexer: Truth-table verification and realization of Half subtractor and Full subtractor.
8. Flip Flops: Truth-table verification of JK Master Slave FF, T-type and D-type FF.
9. Asynchronous Counter: Realization of 4-bit up counter and Mod-N counter.
10. Synchronous Counter: Realization of 4-bit up/down counter and Mod-N counter.
11. Shift Register: Study of shift right, SIPO, SISO, PIPO, PISO & Shift left operations.
12. DAC Operation: Study of 8-bit DAC , obtain staircase waveform.
13. ADC Operations: Study of 8-bit ADC

Data Structures and Algorithms Lab Using C

Course code	LC-CSE-213G				
Category	Professional Core Course				
Course title	Data Structures and Algorithms Lab Using C				
Scheme and Credits	L	T	P	Credits	Semester-3
	0	0	4	2	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	25 Marks				
Total	50 Marks				
Duration of Exam	03 Hours				

Data Structures Lab List of practical exercises, to be implemented using object-oriented approach in C++ Language.

1. Write a menu driven program that implements following operations (using separate functions) on a linear array:
 - Insert a new element at end as well as at a given position
 - Delete an element from a given whose value is given or whose position is given
 - To find the location of a given element
 - To display the elements of the linear array
2. Write a menu driven program that maintains a linear linked list whose elements are stored in on ascending order and implements the following operations (using separate functions):
 - Insert a new element
 - Delete an existing element
 - Search an element
 - Display all the elements
3. Write a program to demonstrate the use of stack (implemented using linear array) in converting arithmetic expression from infix notation to postfix notation.
4. Program to demonstrate the use of stack (implemented using linear linked lists) in evaluating arithmetic expression in postfix notation.
5. Program to demonstration the implementation of various operations on a linear queue represented using a linear array.
6. Program to demonstration the implementation of various operations on a circular queue represented using a linear array.
7. Program to demonstration the implementation of various operations on a queue represented using a linear linked list (linked queue).

8. Program to illustrate the implementation of different operations on a binary search tree.
9. Program to illustrate the traversal of graph using breadth-first search
10. Program to illustrate the traversal of graph using depth-first search.
11. Program to sort an array of integers in ascending order using bubble sort.
12. Program to sort an array of integers in ascending order using selection sort.
13. Program to sort an array of integers in ascending order using insertion sort.
14. Program to sort an array of integers in ascending order using radix sort.
15. Program to sort an array of integers in ascending order using merge sort.
16. Program to sort an array of integers in ascending order using quick sort.
17. Program to sort an array of integers in ascending order using heap sort.
18. Program to sort an array of integers in ascending order using shell sort.
19. Program to demonstrate the use of linear search to search a given element in an array.
20. Program to demonstrate the use of binary search to search a given element in a sorted array in ascending order.

LC-ECE203G

Electronic Devices Lab

L T P Credits
- - 2 1

Class Work : 25 Marks
Theory : 25Marks
Total : 50 Marks
Duration of Exam. : 3 Hrs.

Course Objective: The objectives of this course are as under:

1. To introduce students to the characteristics of diodes, transistors, JFETs, and op-amps .
2. To provide understanding about the operation and characteristics of different configurations of BJT.
3. To provide understanding about the operation and characteristics of different special semiconductor devices.

LIST OF EXPERIMENTS:

- 1 Analysis & study of half wave and full wave rectifiers
- 2 Analysis & study of power supply filter.
- 3 Analysis & study of diode as a clipper and clamper.
- 4 Analysis & study of zener diode as a voltage regulator.
- 5 Analysis & study of CE amplifier for voltage, current and Power gains input, output impedances.
- 6 Analysis & study of CC amplifier as a buffer.
- 7 Analysis & study the frequency response of RC coupled amplifier.
- 8 Analysis & study of transistor as a constant current source in CE configuration .
- 9 To study characteristics of FET.
- 10 Analysis & study of FET common source amplifier.
- 11 Analysis & study of FET common drain amplifier.
- 12 Study and design of a DC voltage doubler.
- 13 To study characteristics of SCR.
- 14 To study characteristics of DIAC.
- 15 To study UJT as a relaxation oscillator.

Course Outcomes: At the end of the course, students will be able to:

1. Understand the characteristics of diodes, transistors, JFETs, and op-amps.
2. Understand the operation and characteristics of different configurations of BJT.
3. Understand the operation and characteristics of different special semiconductor devices.

Note:-

- 1 Total ten experiments are to be performed in the semester.
- 2 At least seven experiments should be performed from the above list. Remaining three experiments should be performed as designed and set by the concerned institution as per the scope of the syllabus.
- 3 At least 5 experiments have to be simulated and results to be validated with experimental results.

Discrete Mathematics

Course code	PCC-CSE-202G				
Category	Professional Core Course				
Course title	Discrete Mathematics				
Scheme and Credits	L	T	P	Credits	Semester - 4
	3	1		4	
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Unit-I

Sets, Relation, Function and Propositional Logic: Operations and Laws of Sets, Cartesian Products, Representation of relations, Binary Relation, Equivalence Relation, Partial Ordering Relation, POSET, Hasse Diagram, Lattices and its types, Function, Bijective functions, Inverse and Composite Function, Finite and infinite Sets, Countable and Uncountable Sets, Cantor's diagonal argument and The Power Set theorem, Schroeder-Bernstein theorem, Propositions, Logical operations, Conditional Statements, Tautologies, Contradictions, Logical Equivalence, The use of Quantifiers

Unit-II

Basic Counting Techniques and Recurrence Relation: Pigeon-hole principle, Permutation and Combination, the Division algorithm: Prime Numbers, The GCD: Euclidean Algorithm, The Fundamental Theorem of Arithmetic., Linear recurrence relation with constant coefficients, Homogenous Solutions, Particular Solutions, Total Solutions, Solving recurrence relation using generating functions

Unit-III

Algebraic Structures: Definitions and examples of Algebraic Structures with one Binary Operation: Semi Groups, Monoids, Groups; Congruence Relation and Quotient Structures, Permutation Groups, Cyclic groups, Normal Subgroups, Definitions and examples of Algebraic Structures with two Binary Operation: Rings, Integral Domain, Fields; Boolean Algebra and Boolean Ring, Identities of Boolean Algebra, Duality, Representation of Boolean Function, Disjunctive and Conjunctive Normal Form

Unit-IV

Graphs and Trees: Graphs and their properties, Degree, Connectivity, Path, Cycle, Sub Graph, Isomorphism, Multigraph and Weighted graph, Shortest path in Weighted graphs, Eulerian paths and circuits, Hamiltonian path and circuits, Planar Graphs, Euler's formulae, Graph Colouring, Trees, Binary trees and its traversals, Trees Sorting, Spanning tree, Minimal Spanning tree

Reference Books:

1. Kenneth H. Rosen, Discrete Mathematics and its Applications, Tata McGraw – Hill
2. Satinder Bal Gupta: A Text Book of Discrete Mathematics and Structures, University Science Press, Delhi.
3. C. L. Liu and D. P. Mohapatra, Elements of Discrete Mathematics A Computer Oriented Approach, Tata McGraw – Hill.
4. J.P. Tremblay and R. Manohar, Discrete mathematical structures with applications to computer science, TMG Edition, TataMcgraw-Hill
5. Discrete Mathematics, Babu Ram, Pearson Publication
6. Discrete Mathematics, Semyour Lipschutz and Marc Lipson, Schaum's outline

Course Outcomes

The students will learn

1. To solve mathematical problems based on concepts of set theory, relations, functions and lattices.
2. To express logic sentence in terms of quantifiers and logical connectives.
3. To apply basic counting techniques to solve permutation and combination problems.
4. To solve recurrence relations.
5. To classify algebraic structure of any given mathematical problem.
6. To evaluate Boolean functions and simplify expressions using the properties of Boolean algebra
7. To develop the given problem as graph networks and solve with techniques of graph theory.

Computer Organization & Architecture

Course code	PCC-CSE-204G				
Category	Professional Core Course				
Course title	Computer Organization & Architecture				
Scheme and Credits	L	T	P	Credits	Semester-IV
	3	0	0	3	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Objectives of the course:

To expose the students to the following:

- How Computer Systems work & the basic principles
- Instruction Level Architecture and Instruction Execution
- The current state of art in memory system design
- How I/O devices are accessed and its principles.
- To provide the knowledge on Instruction Level Parallelism
- To impart the knowledge on micro programming
- Concepts of advanced pipelining techniques.

Unit 1

Data representation: Data Types, Complements, Fixed-Point Representation, Conversion of Fractions, Floating-Point Representation, Gray codes, Decimal codes, Alphanumeric codes, Error Detection Codes.

Register Transfer and Microoperations : Register Transfer Language, Register Transfer, Bus and Memory Transfers, Arithmetic Microoperations, Logic Microoperations, Shift Microoperations, Arithmetic Logic Shift Unit.

Unit 2

Basic Computer Organization and Design : Instruction Codes, Computer Registers, Computer Instructions, Timing and Control, Instruction Cycle, Memory-Reference Instruction, Input-Output Instruction, Complete Computer Description, Design of Basic Computer, Design of Accumulator Logic.

Central Processing Unit : General Register Organization, Stack organization, Instruction Format, Addressing Modes, Data Transfer and Manipulation, Program Control, RISC, CISC.

Unit 3

Pipelining: Basic Concepts of Pipelining, Throughput and Speedup, Pipeline Hazards.

Parallel Processors: Introduction to Parallel Processors, Concurrent access to memory and Cache Coherency.

Unit 4

Input-output Organization : I/O device interface, I/O transfers—program controlled, interrupt driven and DMA, Privileged and Non-Privileged Instructions, Software Interrupts.

Memory organization: Memory Hierarchy, Main Memory, Auxiliary Memory, Associative Memory, Cache Memory, Associative Mapping, Direct Mapping, Set-Associative Mapping, Writing into Cache, Cache Initialization, Virtual Memory.

Suggested books:

- 1) "Computer System Architecture", 3rd Edition by M.Morris Mano, Pearson.
- 2) "Computer Organization and Design: The Hardware/Software Interface", 5th Edition by David A. Patterson and John L. Hennessy, Elsevier.
- 3) "Computer Organization and Embedded Systems", 6th Edition by Carl Hamacher, McGraw Hill Higher Education.

Suggested reference books:

- 1) "Computer Architecture and Organization", 3rd Edition by John P. Hayes, WCB/McGraw-Hill
- 2) "Computer Organization and Architecture: Designing for Performance", 10th Edition by William Stallings, Pearson Education.
- 3) "Computer System Design and Architecture", 2nd Edition by Vincent P. Heuring and Harry F. Jordan, Pearson Education.

Course outcomes :

- 1) Draw the functional block diagram of a single bus architecture of a computer and describe the function of the instruction execution cycle, RTL interpretation of instructions, addressing modes, instruction set.

2) Write assembly language program for specified microprocessor for computing

16 bit multiplication, division and I/O device interface (ADC, Control circuit, serial port communication).

- 3) Write a flowchart for Concurrent access to memory and cache coherency in Parallel Processors and describe the process.
- 4) Given a CPU organization and instruction, design a memory module and analyze its operation by interfacing with the CPU.
- 5) Given a CPU organization, assess its performance, and apply design techniques to enhance performance using pipelining, parallelism and RISC methodology.

Operating System

Course code	PCC-CSE-206G				
Category	Professional Core Course				
Course title	Principles of Operating System				
Scheme and Credits	L	T	P	Credits	Semester-4
	3	0	0	3	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

UNIT 1:

Introduction: Concept of Operating Systems, Generations of Operating systems, Types of Operating Systems, OS Services.

Processes: Definition, Process Relationship, Different states of a Process, Process State transitions, Process Control Block (PCB), Context switching. Thread: Definition, Various states, Benefits of threads, Types of threads, Multithreading.

Process Scheduling: Foundation and Scheduling objectives, Types of Schedulers, Scheduling criteria: CPU utilization, Throughput, Turnaround Time, Waiting Time, Response Time; Scheduling algorithms: Pre-emptive and Non-pre-emptive, FCFS, SJF, SRTF, RR Scheduling.

UNIT 2:

Inter-process Communication: Critical Section, Race Conditions, Mutual Exclusion, The Producer\ Consumer Problem, Semaphores, Event Counters, Monitors, Message Passing, Classical IPC Problems: Reader's & Writer Problem, Dining Philosopher Problem etc.

Deadlocks: Definition, Necessary and sufficient conditions for Deadlock, Deadlock Prevention, and Deadlock Avoidance: Banker's algorithm, Deadlock detection and Recovery.

UNIT 3:

Memory Management: Basic concept, Logical and Physical address map, Memory allocation: Contiguous Memory allocation – Fixed and variable partition–Internal and External fragmentation and Compaction; Paging: Principle of operation – Page allocation – Hardware support for paging, Protection and sharing, Disadvantages of paging.

Virtual Memory: Basics of Virtual Memory – Hardware and control structures – Locality of reference, Page fault, Working Set, Dirty page/Dirty bit – Demand paging, Page Replacement algorithms: Optimal, First in First Out (FIFO), Optimal Page Replacement and Least Recently used (LRU).

UNIT 4:

File Management: Concept of File, Access methods, File types, File operation, Directory structure, File System structure, Allocation methods (contiguous, linked, indexed), efficiency and performance.

Disk Management: Disk structure, Disk scheduling - FCFS, SSTF, SCAN, C-SCAN, Disk reliability, Disk formatting, Boot-block, Bad blocks. Case study on UNIX and WINDOWS Operating System.

Suggested books:

- Operating System Concepts Essentials, 9th Edition by Avi Silberschatz, Peter Galvin, Greg Gagne, Wiley Asia Student Edition.
- Operating Systems: Internals and Design Principles, 5th Edition, William Stallings, Prentice Hall of India.

Suggested reference books:

- Operating System: A Design-oriented Approach, 1st Edition by Charles Crowley, Irwin Publishing
- Operating Systems: A Modern Perspective, 2nd Edition by Gary J. Nutt, Addison-Wesley
- Design of the Unix Operating Systems, 8th Edition by Maurice Bach, Prentice-Hall of India
- Understanding the Linux Kernel, 3rd Edition, Daniel P. Bovet, Marco Cesati, O'Reilly and Associates

Course Outcomes:

CO1: Understand the structure and architectural components of OS to analyze and design the applications to run in parallel. Moreover, students would be able to develop scheduling algorithms to optimize various parameters like CPU utilization, Throughput, Turnaround Time, Waiting Time, Response Time for research purpose.

CO2: Understand the design issues associated with Operating system (e.g. Mutual exclusion, Deadlock detection etc.) to gain insight towards developing algorithms/techniques for efficient deadlock handling.

CO3: For a given specification of memory organization, develop the techniques for optimally allocating memory to processes by increasing memory utilization and for improving the access time.

CO4: Design and implement file management system for a given specification. Identify, use and evaluate the disk management policies with respect to various performance evaluation parameters.

Object Oriented Programming

Course code	PCC-CSE-208G				
Category	Professional Core Course				
Course title	Object Oriented Programming				
Scheme and Credits	L	T	P	Credits	Semester-4
	3	0	0	3	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	75 Marks				
Total	100 Marks				
Duration of Exam	03 Hours				

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Unit - I

Object-Oriented Programming Concepts: Introduction, comparison between procedural programming paradigm and object-oriented programming paradigm, basic concepts of object-oriented programming — concepts of an object and a class, interface and implementation of a class, operations on objects, relationship among objects, abstraction, encapsulation, data hiding, inheritance, overloading, polymorphism, messaging.

Classes and Objects: Specifying a class, creating class objects, accessing class members, access specifiers, static members, use of const keyword, friends of a class, empty classes, nested classes, local classes, abstract classes, container classes, bit fields and classes.

Unit - II

Inheritance: Introduction, defining derived classes, forms of inheritance, ambiguity in multiple and multipath inheritance, virtual base class, object slicing, overriding member functions, object composition and delegation, order of execution of constructors and destructors.

Pointers and Dynamic Memory Management: Declaring and initializing pointers, accessing data through pointers, pointer arithmetic, memory allocation (static and dynamic), dynamic memory management using new and delete operators, pointer to an object, this pointer, pointer related problems - dangling/wild pointers, null pointer assignment, memory leak and allocation failures.

Unit - III

Constructors and Destructors: Need for constructors and destructors, copy constructor, dynamic constructors, explicit constructors, destructors, constructors and destructors with static members, initializer lists.

Operator Overloading and Type Conversion: Overloading operators, rules for overloading operators, overloading of various operators, type conversion - basic type to class type, class type to basic type, class type to another class type.

Virtual functions & Polymorphism: Concept of binding - early binding and late binding, virtual functions, pure virtual functions, abstract classes, virtual destructors.

Unit - IV

Exception Handling: Review of traditional error handling, basics of exception handling, exception handling mechanism, throwing mechanism, catching mechanism, rethrowing an exception, specifying exceptions.

Templates and Generic Programming: Template concepts, Function templates, class templates, illustrative examples.

TEXT BOOKS, AND/OR REFERENCE MATERIAL:

1. Bjarne Stroustrup, "C++ Programming language", 3rd edition, Pearson education Asia (1997)
2. Lafore R. "Object oriented Programming in C++", 4th Ed. Techmedia, New Delhi (2002).
3. Yashwant Kenetkar, "Let us C++", 1st Ed., Oxford University Press (2006)
4. B.A. Forouzan and R.F. Gilberg, Compiler Science, "A structured approach using C++" Cengage Learning, New Delhi.

Course code	HSMC-02G				
Category					
Course title	ORGANIZATIONAL BEHAVIOUR				
Scheme and Credits	L	T	P	Credits	
	3	0	0	3	
Branches (B. Tech.)					
Class work	25				
Exam	75				
Total	100 Marks				
Duration of Exam	03 Hours				

The objective of this course is to expose the students to basic concepts of management and provide insights necessary to understand behavioral processes at individual, team and organizational level.

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

SYLLABUS

UNIT - 1

Introduction of Management- Meaning, definitions, nature of management; Managerial levels, skills and roles in an organization; Functions of Management: Planning, Organizing, staffing, Directing & Controlling, Interrelationship of managerial functions, scope of management & Importance of management. Difference between management and administration.

UNIT - 2

Introduction of organization:- Meaning and process of Organization, Management v/s Organization; **Fundamentals of Organizational Behavior:** Concepts, evolution, importance and relationship with other Fields; Contemporary challenges and opportunities of OB. **Individual Processes and Behavior-Personality-** Concept, determinants and applications; **Perception-** Concept, process and applications, **Learning-** Concept (Brief Introduction) ; **Motivation-** Concept, techniques and importance

UNIT - 3

Interpersonal Processes- Teams and Groups- Definition of Group, Stages of group development, Types of groups, meaning of team, merits and demerits of team; difference between team and group, **Conflict-** Concept, sources, types, management of conflict; **Leadership:** Concept, function, styles & qualities of leadership. **Communication –** Meaning, process, channels of communication, importance and barriers of communication.

UNIT 4

Organizational Processes: Organizational structure - Meaning and types of organizational structure and their effect on human behavior; **Organizational culture -** Elements, types and factors affecting organizational culture. **Organizational change:** Concept, types & factors affecting organizational change, Resistance to Change.

Course Outcomes: By the end of this course the student will be able to:

1. Students will be able to apply the managerial concepts in practical life.
2. The students will be able to understand the concept of organizational behavior at individual level and interpersonal level.
3. Students will be able to understand the behavioral dynamics in organizations.
4. Students will be able to understand the organizational culture and change

Suggested Books:

1. Robbins, S.P. and Decenzo, D.A. Fundamentals of Management, Pearson Education Asia, New Delhi.
2. Stoner, J et. al, Management, New Delhi, PHI, New Delhi.
3. Satya Raju, Management – Text & Cases, PHI, New Delhi.
4. Kavita Singh, Organisational Behaviour: Text and cases. New Delhi: Pearson Education.
5. Pareek, Udai, Understanding Organisational Behaviour, Oxford University Press, New Delhi.
6. Robbins, S.P. & Judge, T.A., Organisational Behaviour, Prentice Hall of India, New Delhi.
7. Ghuman Karminder, Aswathappa K., Management concept practice and cases, Mc Graw Hill education.
8. Chhabra T. N., Fundamental of Management, Sun India Publications-New Delhi.

PCC-ECE202G

Communication System

L	T	P	Credits
3	0	0	3

Class Work : 25 Marks
Theory : 75 Marks
Total : 100 Marks
Duration of Exam: 3 Hrs.

Course Objective: The objectives of this course are as under:

- To introduce the students to the basics of different types of modulation techniques
- To aim at a comprehensive coverage of design of radio transmitter and receiver

Unit 1

Introduction To Communication System: Modulation, Demodulation, Radio Frequency Spectrum, Signals & their classification, Limitations & Advantages of a Communication System, Comparison of Analog & Digital Communication Systems, Historical Perspective, Modes & Medias of Communication.

Noise: Sources of Noise, External & Internal Noise, Noise Calculations, Noise Figure, Noise Figure Calculation, Noise Temperature, Noise in Communication Systems, Band Pass Noise Model, Cascaded Stages & its Noise Figure Calculation, Signal in presence of Noise, Pre-Emphasis & De-Emphasis, Noise Quieting Effect, Capture Effect, Noise in Modulation Systems.

Unit 2

Linear Modulation: (AM) Basic definition & derivation for Modulation & Modulation Index, Modulation & Demodulation of AM, Suppressed Carrier Modulation, Quadrature Amplitude Modulation, SSB-SC, DSB-SC, VSB Modulation & Demodulation, Comparison of various AM Systems, Generation of AM waves.

Angle Modulation:

Basic definition & derivation for Modulation & Modulation Index, Generation of FM waves, Comparison between PM & FM, Frequency Spectrum of FM, B.W. & required spectra, Types of FM, vector representation of FM, Universal Curve, Multiple FM, Demodulation of FM waves, Demodulation of PM waves, Comparison between AM & FM.

Unit 3

Transmitters & Receivers: Classification of Radio Transmitters, Basic Block Diagram of Radio Transmitter, Effect of Feedback on operation of Transmitter, Radio Telephone Transmitters, Privacy Device in Radio Telephony, FM Transmitter using Reactance Modulator, Armstrong FM Transmitter, Radio Receivers, Classification, TRF Receiver, Super Heterodyne Receiver, Image Rejection & Double Spotting, Choice of IF, Tracking & Alignment of Receivers, AGC.

Pulse Analog Modulation: Sampling theory, TDM, FDM, PAM, PWM, PPM, Modulation & Demodulation techniques of above all.

UNIT 4

Pulse Digital Modulation: Elements of Pulse Code Modulation, Noise in PCM Systems, Bandwidth of PCM Systems, Measure of Information, Channel Capacity, Channel Capacity of PCM System, Differential Pulse Code Modulation (DPCM). Delta Modulation (DM)

Digital Carrier Modulation And Demodulation Techniques:Digital Modulation Formats, Coherent Binary Modulation & Demodulation: ASK, BPSK, BFSK, Coherent Quadrature Modulation & Demodulation Techniques: QPSK, MSK.

Non Coherent BFSK, Differential PSK, M-Ary Modulation & Demodulation Techniques: M-Ary PSK, M-Ary QAM, M-Ary FSK, Synchronization: Carrier & Symbol Synchronization.

Reference Books:

- | | |
|-------------------------------------|--|
| 1. Communication Systems | By Manoj Duhan – I. K. International |
| 2. Electronic Communication Systems | By Kennedy – TMH |
| 3. Communication Systems | By Singh & Sapre – TMH |
| 4. Communication System Engineering | By John G. Proakis and Masoud Salehi, Pearson Education, 2015. |
| 5. Analog Communication | By P. Chakarbarti – DR & Co. |
| 6. Communication Systems | By Simon Haykins – Wiley |

COURSE OUTCOMES:

- Student will be familiar with concept of modulation and various modulation techniques
- Ability to model noise in communication systems
- Familiarity with design of radio transmitter and receiver

Note: Examiner will set nine questions in total. Question one will be compulsory. Question one will have 6 parts of 2.5 marks each from all units and remaining eight questions of 15 marks each to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each unit.

Operating System Lab

Course code	LC-CSE-212G				
Category	Professional Core Course				
Course title	Operating System Lab				
Scheme and Credits	L	T	P	Credits	
	0	0	4	2	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	25 Marks				
Total	50 Marks				
Duration of Exam	03 Hours				

Contents:

- 1 Introduction to UNIX File System.
2. File and Directory Related Commands in UNIX.
3. Essential UNIX Commands for working in UNIX environment.
4. I/O Redirection and Piping
5. Introduction to VI Editors.
6. Introduction of Processes in UNIX
7. Communication in UNIX and AWK.
8. Introduction of the concept of Shell Scripting.
9. Decision and Iterative Statements in Shell Scripting.
10. Writing the Shall Scripts for unknown problems.

Suggested Books:

1. UNIX Shell Programming by Yashavant Kanetkar.
2. UNIX Concepts and Applications by Sumitabha Das

Course Outcomes.

Co1: Understand the structure and architectural components of UNIX Operating System to analyze and design the problem. Moreover, students would be able to know the Basic Introduction of UNIX Operating System.

Co2: Basic Introduction of UNIX Commands that are used for operating the UNIX.

Co3: Introduction of Shell Scripting and VI Editor.so that the students get familiar with writing the UNIX scripts in UNIX editor.

Co4: Students will establish themselves as effective professionals by solving real problems with UNIX Shell Scripting knowledge and with attention to teamwork, critical thinking and problem solving skills by Writing Shell Scrips of unknown problems

Object Oriented Programming Lab Using C++

Course code	LC-CSE-214G				
Category	Professional Core Course				
Course title	Object Oriented Programming Lab Using C++				
Scheme and Credits	L	T	P	Credits	
	0	0	4	2	
Branches (B. Tech.)	Computer Science and Engineering				
Class work	25 Marks				
Exam	25 Marks				
Total	50 Marks				
Duration of Exam	03 Hours				

Contents:

1. [Classes and Objects] Write a program that uses a class where the member functions are defined inside a class.
2. [Classes and Objects] Write a program that uses a class where the member functions are defined outside a class.
3. [Classes and Objects] Write a program to demonstrate the use of static data members.
4. [Classes and Objects] Write a program to demonstrate the use of const data members.
5. [Constructors and Destructors] Write a program to demonstrate the use of zero argument and parameterized constructors.
6. [Constructors and Destructors] Write a program to demonstrate the use of dynamic constructor.
7. [Constructors and Destructors] Write a program to demonstrate the use of explicit constructor.
8. [Initializer Lists] Write a program to demonstrate the use of initializer list.
9. [Operator Overloading] Write a program to demonstrate the overloading of increment and decrement operators.
10. [Operator Overloading] Write a program to demonstrate the overloading of binary arithmetic operators.
11. [Operator Overloading] Write a program to demonstrate the overloading of memory management operators.
12. [Inheritance] Write a program to demonstrate the multilevel inheritance.
13. [Inheritance] Write a program to demonstrate the multiple inheritance.
14. [Inheritance] Write a program to demonstrate the virtual derivation of a class.
15. [Polymorphism] Write a program to demonstrate the runtime polymorphism.
16. [Exception Handling] Write a program to demonstrate the exception handling.

17. [Templates and Generic Programming] Write a program to demonstrate the use of function template.

18. [Templates and Generic Programming] Write a program to demonstrate the use of class template.

L	T	P	Credits
-	-	2	1

Class Work	:	25 Marks
Theory	:	25Marks
Total	:	50 Marks
Duration of Exam.	:	3 Hrs.

COURSE OBJECTIVES:

- To provide the basic understanding about various modulation techniques.
- To analyze different characteristic parameters of these modulation techniques.

LIST OF EXPERIMENTS:

1. To study and waveform analysis of amplitude modulation and determine the modulation index of amplitude modulation.
2. To study and waveform analysis of amplitude demodulation by any method.
3. To study and waveform analysis of frequency modulation and determine the modulation index of frequency modulation.
4. To study and waveform analysis of frequency demodulation by any method.
5. To study Amplitude Shift Keying (ASK) modulation.
6. To study Frequency Shift Keying (FSK) modulation.
7. To study Phase Shift Keying (PSK) modulation.
8. To study and waveform analysis of phase modulation.
9. To study Phase demodulation.
10. To study Pulse code modulation.
11. To study Pulse amplitude modulation and demodulation.
12. To study Pulse width modulation.
13. To study Pulse position modulation.
14. To study delta modulation.
15. To deliver a seminar by each student on ADVANCE COMMUNICATION SYSTEM.

COURSE OUTCOMES:

- Students are able to analyze digital communication signals.
- Students understand the basics of PAM, QAM, PSK, FSK, and MSK.
- They can analyze noise and disturbance in modulated signals.

Note:-

1 Total ten experiments are to be performed in the semester
At least seven experiments should be performed from the above list. Remaining three experiments should be performed as designed and set by the concerned institution as per the scope of the syllabus.

MC-106G : (ENVIRONMENT SCIENCE)

Theory 75 Marks Field Work 25 Marks (Practical/Field visit)

Unit-1 The Multidisciplinary nature of environment studies. Definition, scope and importance. (2 lecture)

Unit-2 Natural Resources :

Renewable and non-renewable resources :

Natural resources and associated problems.

- a) Forest resources : Use and over-exploitation : deforestation, case studies. Timber extraction, mining dams and their effects on forests and tribal people.
 - b) Water resources : Use and over-utilization of surface and ground water, floods, drought, conflicts over water, dams-benefits and problems.
 - c) Mineral resources : Use and exploitation, environmental effects of extracting and using mineral resources, case studies.
 - d) Food resources : World food problems, changes, caused by agriculture and overgrazing, effects of modern agriculture, fertilizer-pesticide problems, Water logging, salinity, case studies.
 - e) Energy resources : Growing energy needs; renewable and non-renewable energy sources, use of alternate energy sources, case studies.
 - f) Land resources : Land as a resource, land degradation, man induced landslides, soil erosion and desertification.
- * Role of an individual in conservation of natural resources.
 - * Equitable use of resources for sustainable lifestyles.

(8 lectures)

Unit-3 Ecosystems :

- * Producers, consumers and decomposers.
- * Energy flow in the ecosystem.
- * Ecological succession.
- * Food chains, food webs and ecological pyramids.
- * Introduction, types, characteristic features, structure and function of the following eco-system :
 - a. Forest ecosystem.
 - b. Grassland ecosystem.
 - c. Desert ecosystem.
 - d. Aquatic ecosystems (ponds, streams, lakes, rivers, oceans, estuaries) (6 lectures)

Unit-4 Biodiversity and its conservation

- * Introduction - Definition : Genetic, Species and ecosystem diversity.
- * Value of biodiversity: consumptive use, productive use, social, ethical, aesthetic and option values.
- * Biodiversity at global, National and local levels.
- * India as a mega-diversity nation.
- * Hot-spots of biodiversity.
- * Threats to biodiversity: habitat loss, poaching of wildlife, man-wildlife conflicts.
- * Endangered and endemic species of India.
- * Conservation of biodiversity : In-situ and ex-situ conservation of biodiversity.

(8 lectures)

Unit-5 Environmental pollution :

Definition, causes, effects and control measures of :

- a) Air pollution.
- b) Water pollution
- c) Soil pollution
- d) Marine pollution
- e) Noise pollution
- f) Thermal pollution
- g) Nuclear hazards
- * Solids waste management: causes, effects and control measures of urban and industrial wastes.
- * Role of an individual in prevention of pollution.
- * Pollution case studies.
- * Disaster management : floods, earthquake, cyclone and landslides.

(8 lectures)

Unit-6 Social issues and the Environment:

- * From unsustainable to sustainable development.
- * Urban problems related to energy.
- * Water conservation, rain water harvesting, watershed management.
- * Resettlement and rehabilitation of people : its problems and concerns case studies.
- * Environmental ethics : Issues and possible solutions.
- * Climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust. Case studies.
- * Wasteland reclamation.

- * Consumerism and waste products.
- * Environment Protection Act.
- * Air (Prevention and Control of pollution) Act.
- * Water (Prevention and Control of pollution) Act.
- * Wildlife Protection Act.
- * Forest Conservation Act.
- * Issues involved in enforcement of environmental legislation.

* Public awareness. (7 lectures)

Unit-7 Human population and the Environment.

Population growth, variation among nations.

Population explosion- Family Welfare Programme.

Environment and human health.

Human Rights.

Value Education.

HIV/AIDS.

Woman and Child Welfare

Role of Information Technology in Environment and human health.

Case Studies. (6 lectures)

Unit-8 Field Work :

- * Visit to a local area to document environmental assets - river/forest/grassland/hill/mountain.
- * Visit to a local polluted site-urban/Rural/ Industrial/ Agricultural.
- * Study of common plants, insects, birds.
- * Study of simple ecosystems- pond, river, hill slopes, etc. (Field work equal to 10 lecture hours).

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(M) Magazine
(R) Reference
(TB) Textbook

The scheme of the paper will be under :

The subject of Environmental Studies will be included as a qualifying paper in all UG Courses and the students will be required to qualify the same otherwise the final result will not be declared and degree will not be awarded.

The duration of the course will be 40 lectures. The examination will be conducted along with the semester examinations.

Exam. Pattern : In case of awarding the marks, the paper will carry 100 marks. Theory: 75 marks, Practical/ Field visit : 25 marks.

The structure of the question paper will be :

Part- A: Short Answer Pattern : 15marks

Part- B : EssayType with inbuilt choice : 60marks

Part-C : Field Work (Practical) : 25marks

Instructions for Examiners :

Part- A : Question No. 1 is compulsory and will contain five short- answer type question of 3 marks each covering the entire syllabus.

Part-B : Eight essay type questions (with inbuilt choice) will be set from the entire syllabus and the candidate will be required to answer any four of them. Each essay type question will be of 15 marks.

The examination of the regular students will be conducted by the concerned college/Institute. Each student will be required to score minimum 40% marks separately in theory and practical/Field visit. The marks in this qualifying paper will not be included in determining the percentage of marks obtained for the award of degree. However, these marks will be shown in the detailed marks certificate of the students.