

**GURUGRAM UNIVERSITY**  
**SCHEME OF STUDIES AND EXAMINATION**  
**M.TECH 1st YEAR (ELECTRONICS & COMMUNICATION ENGINEERING)**  
**SEMESTER - 1**

Sl. No	Course Code	Subject	Credit Pattern				Examination Schedule (Marks)				Duration of Exam (Hours)	No of Hours /week
			L	T	P	Total Credits	Marks of Class work	Theory	Practical	Total		
1	16ECE21C1	Advance Microprocessor & Microcontroller	4	0	-	4	50	100	-	150	3	4
2	16ECE21C2	Satellite and Space Communication	4	0	-	4	50	100	-	150	3	4
3	16ECE21C3	Information and Communication Theory	4	0	-	4	50	100	-	150	3	4
4	16ECE21C4	Advanced Digital Signal Processing	4	0	-	4	50	100	-	150	3	4
5	16ECE21C5	Data Communication Networks	4	0	-	4	50	100	-	150	3	4
6	16ECE21C6	Seminar	-	-	-	2	50	-	-	50		2
7	16ECE21CL1	Satellite Lab	-	-	2	2	50	-	50	100	3	4
8	16ECE21CL2	Advance Microprocessor & Microcontroller Lab	-	-	2	2	50	-	50	100	3	4
		<b>TOTAL</b>					<b>26</b>					

NOTE:

**Examiner will set nine question in total. Question One will be compulsory and will comprise short answer type questions from all sections and remaining eight questions to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each Unit.**

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**SEMESTER 2**

Sl No	Course No.	Subject	Credit Pattern			Examination Schedule (Marks)				Duration of Exam (Hours)
			T	P	Total Credits	Marks of Class works	Theory	Practical	Total	
1	16ECE22C1	Wireless Mobile Communication	4	0	4	50	100	-	150	3
2	16ECE22C2	Optical Communication	4	0	4	50	100	-	150	3
3	16ECE22C3	Seminar	-	-	2	50	-	-	50	
4	16ECE22CL1	VLSI Lab	-	-	2	50	-	50	100	3
5	16ECE22CL2	Optical Communication Lab	-	-	2	50	-	50	100	3
6	16ECE22D1 or 16ECE22D2 or 16ECE22D3 or 16ECE22D4	Elective-1	4	0	4	50	100	-	150	3
7		Open Elective			3					
8		Foundation Elective			2					
		<b>TOTAL</b>	<b>23</b>							

**NOTE:** Examiner will set nine questions in total. Question One will be compulsory and will comprise short answer type questions from all sections and remaining eight questions to be set by taking two questions from each unit. The students have to attempt five questions in total, first being compulsory and selecting one from each Unit.

**Elective 1 : Choose any one from the following four papers:**

16ECE22D1 - Electronic System Design

16ECE22D2 - Image Processing

16ECE22D3 – ADVANCED MATHEMATICS FOR ENGINEERS

16ECE22D4 - VLSI Design

**Open Elective:** A candidate has to select this paper from the pool of Open Electives provided by the University.

**Foundation Elective:** A candidate has to select this paper from the pool of Foundation Electives provided by the University.