5th IEEE Delhi Section International Conference
IEEE DELCON 2026
19-21, November 2026

Technically Co-Sponsored by IEEE Delhi Section, India
Converging Technologies for Sustainable Innovation

Technical Tracks

- **Track 1.** Smart Communication & Networking Technologies
- **Track 2.** Quantum Technologies & Photonic Systems
- **Track 3.** Intelligent Systems – AI, ML & Data Science
- **Track 4.** VLSI, Embedded Systems & Next-Gen Computing
- **Track 5.** Signal Processing, Imaging & Multimedia Systems
- **Track 6.** Sustainable Energy, Power Electronics & Smart Grids
- **Track 7.** Cyber-Physical Systems, Security & IoT Applications
- **Track 8.** Advanced Materials & Emerging Devices
- **Track 9.** Antenna Design, Metamaterials & Microwave Engineering
- **Track 10.** Robotics, Control & Autonomous Systems

Important Dates

Paper Submission Opens	December 01, 2025
Paper Submission Deadline	July 01, 2026
Acceptance Notification	September 15, 2026
Camera-Ready Submission	October 05, 2026

Submission Guidelines

Authors are invited to submit original research contributions in IEEE double-column format (6 pages max). All submissions will undergo a double-blind peer review. All papers should be submitted via edas.

Accepted Papers, after presentation, will be submitted to IEEE Xplore for publication.

Explore More

www.bits-pilani.ac.in/delcon2026

delcon2026@pilani.bits-pilani.ac.in

BITS Pilani, Pilani Campus, Rajasthan

IEEE DELCON is the flagship international conference of the IEEE Delhi Section, one of the 13 sections in India under the IEEE Asia-Pacific Region (Region 10).

DELCON began in 2022 at NSUT, New Delhi, and has since been hosted by Chitkara University and BVPCE, New Delhi. The 2025 edition was organized by Jamia Millia Islamia. The fifth edition of IEEE DELCON 2026 will be hosted at BITS Pilani, Rajasthan. Technically sponsored by the IEEE Delhi Section, DELCON 2026 will continue to provide a premier platform for researchers in Electrical, Computer, and Electronics Engineering, with accepted papers published in IEEE Xplore and indexed by SCOPUS and Google Scholar.

- **Plenary and Keynote Talks**
- **Best paper award in each track**
- **Startup and Exhibition**
- **Wide visibility and citations**
- **Great networking opportunities**

Organizing Committee

Chief Patrons:
- Prof. V. Ramgopal Rao, Vice-Chancellor, BITS Pilani

Patrons:
- Prof. Sudhirkumar Barai, Director, BITS Pilani, Rajasthan
- Prof. M N Hoda, Chairperson, IEEE Delhi Section

General Chairs:
- Prof. Navneet Gupta, BITS Pilani
- Prof. Sneha Kabra, Secretary, IEEE Delhi Section

Conference Chairs:
- Prof. Syed M. Zafaruddin, BITS Pilani, Rajasthan
- Prof. Jasdeep K. Dhanoa, IEEE Delhi Section

Organizing Committee Chairs:
- Prof. Puneet Mishra, BITS Pilani, Rajasthan
- Prof. Meetha V Shenoy, BITS Pilani, Rajasthan

Technical Program Chairs
- Prof. Arnab Hazra, BITS Pilani, Rajasthan
- Prof. Rajnish Sharma, IEEE Delhi Section