GURUGRAM UNIVERSITY

SCHEME OF STUDIES AND EXAMINATION

M.TECH 2nd YEAR (ELECTRONICS & COMMUNICATION ENGINEERING)

SEMESTER 3rd

Sl. No	Course No.	Subject	Teaching Schedule				Examination Schedule (Marks)				Duration of Exam (Hours)	No of Credits
			L	T	P	Total	Marks of Class works	Theory	Practical	Total		
1	17ECE23C1	Neural Networks & Fuzzy Logics	4	0	-	4	50	100	-	150	**3**	**4**
2	17ECE23C2	CDMA	4	0	-	4	50	100	-	150	**3**	**4**
3	17ECE23C3	DISSERTATION (PHASE-I)	-	-	-	4	100	-	-	100		**2**
4	17ECE23C4	Seminar	-	-	-	2	50	-	-	50		**2**
5	17ECE23CL1	Project	-	-	2	2	50	-	50	100		**2**
6	17ECE23CL2	MATLAB Lab	-	-	2	2	50	-	50	100		**2**
7		OPEN ELECTIVE										**3**
		TOTAL										**21**

NOTE:

1. **Students will be allowed to use non-programmable scientific calculator. However, sharing of Calculator will not be permitted in the examination.**

2. **Students have to publish a research paper in a journal / conference on the basis of literature survey done in the semester.**

OPEN ELECTIVE: A candidate has to select this paper from the pool of open electives provided by the University.

GURUGRAM UNIVERSITY
SCHEME OF STUDIES AND EXAMINATION
M.TECH 2nd YEAR (ELECTRONICS & COMMUNICATION ENGINEERING)
SEMESTER 4th

Sl. No	Course No.	Subject	Teaching Schedule				Examination Schedule (Marks)				No of Credits
			L	T	P	Total	Marks of Class works	Theory	Practical	Total	
	17ECE24C1	Dissertation and viva	-	-	-	-	250	-	500	750	**20**
		TOTAL	-	-	-	-	**250**	-	**500**	**750**	
		GRAND TOTAL									

NOTE:

1. **Students have to publish a research paper in a journal / conference of the research work done in the semester.**